

<b>Applicant:</b>	<b>Zhang</b>	<b>Docket No:</b>	<b>TI-36092</b>
<b>Serial No:</b>	<b>10/664,311</b>	<b>Examiner:</b>	<b>Choe, Henry</b>
<b>Filed:</b>	<b>9/16/2003</b>	<b>Art Unit:</b>	<b>2817</b>
<b>For:</b>	<b>OUTPUT STAGE CIRCUIT FOR AN OPERATIONAL AMPLIFIER</b>		

Assistant Commissioner For Patents  
Washington, DC 20231

Dear Sir:

PAGE 3/16 \* RCVD AT 3/18/2005 5:34:05 PM [Eastern Standard Time] \* SVR:USPTO-EFXRF-1/1 \* DNIS:8729306 \* CSID:972 917 4417 \* DURATION (mm-ss):05-38